



सूक्ष्म कणिका एवं संचार अभियंत्रण विभाग
Department Of Electronics & Communication Engineering
मोतीलालनेहरू राष्ट्रीय प्रौद्योगिकी संस्थान
MOTILAL NEHRU NATIONAL INSTITUTE OF TECHNOLOGY
इलाहाबाद— 211004
ALLAHABAD-211004

Advt. No.: 01/ECED/C2S/Project Staff/2023

Dt.: 04-10-2023

Advertisement for recruitment of Project Staffs under Chips to Start-up (C2S) Project

Project Title	<i>Design and Fabrication of Digitally Assisted CMOS Transmitter Frontend for Indian Long Range (i-LoRa) Application</i>	
Sponsoring Agency	Ministry of Electronics & Information Technology, R&D Electronics Group (Microelectronics Development Division), Govt. of India	
Name of Position	Research Associate (RA) & Project Associate (PA)	
Duration (In Years)	Initially for 12 months, extendable up to 05 Years or the duration of the Project whichever is earlier.	
Number of Positions	02 (RA) + 01 (PA)	
Essential Educational Qualification and Experience	(i) B.E./ B.Tech./ or equivalent degree in Electronics & Communication Engineering - for Project Associate (PA): Desirable - valid GATE score (ii) M.E./M.Tech. in Microelectronics & VLSI Design or related Specializations with valid GATE score – for Research Associate (RA)	
Age	Should not exceed 28 years as on the last date of application.	
Age Relaxation	05 years is applicable for Female/SC/ST/PwD candidate	
Desired Skills	(i) Knowledge of OS (Window/Linux/CentOS/RHEL), Server handling, network file system, knowledge of Cadence/Synopsys/Mentor Graphics/TCAD, etc. (ii) Digital/Analog/RF/Mixed-mode Circuit Design, HDL Coding	
Emoluments	Rs. 35,000/- consolidated(for RA); Rs. 29,000/- consolidated(for PA) per month + 10% increment per annum on satisfactory performance	
Accommodation	The selected candidates are eligible for on-campus Hostel & Mess facilities on payment basis subject to availability.	
Job Nature	Execute the objectives of research project. (i.e. Design and Fabrication of Digitally Assisted CMOS Transmitter Frontend for Indian Long Range (i-LoRa) Application and demonstration of the prototype of the same.)	
Other Benefits	The selected candidates are eligible to register for MTech/PhD degree at MNNIT Allahabad as per institute rules. However He/She needs to apply for the same when the advertisement is open.	
Application Mode	The Performa of application is attached with this advertisement. A soft copy of the completed application with the relevant supporting documents is required to be sent through email to: smdp@mnnit.ac.in	
Last Date To Apply	23 October 2023 (Through Email Only)	
Contacts	Dr. Rajendra Kumar Nagaria, Professor [HAG] Chief Investigator, C2S Project ECE Department MNNIT Allahabad Email: smdp@mnnit.ac.in	Dr. Santosh Kumar Gupta Associate Professor Co-Chief Investigator, C2S Project ECE Department MNNIT Allahabad Email: smdp@mnnit.ac.in
Contact Number	0532-2271475; +91 8004928398	

General Terms & Conditions:

- 1 The position is purely temporary
- 2 The CI has the discretion to restrict the number of candidates to be called for interview to a reasonable limit on the basis of qualifications and experience higher than the minimum prescribed in the advertisement.
- 3 The candidate is required to enclose a brief resume/ curriculum vitae/ bio-data along with the filled application form.
- 4 Only shortlisted candidates will be communicated to appear in the interview and no other communications in this regard will be entertained.
- 5 The candidate is expected to join immediately, if selected.
- 6 No TA/DA will be paid for attending the interview

**APPLICATION FOR THE POSITION OF RA/PA UNDER
CHIPS TO START-UP (C2S) PROJECT**

Advt. No.: _____

Dated: _____

(Please enclose a copy of bio-data/ resume/ curriculum-vitae with supporting documents)

1. Name of the candidate

2. Father's name

**3. Date of birth
(DD/MM/YYYY)**

4. Sex Male/ Female

5. Marital status Single/ Married

6. Category OC/OBC/SC/ST/EWS/PwD

7. Educational qualifications



Degree	Specialization/ subjects	Board/ University	Year of passing	Class	Percentage/ CGPA
High School					
Intermediate					
B.E./B.Tech.					
M.E./M.Tech.					
Ph.D.					
Any other					

8. GATE score earned/ qualified

Specialization	Year appeared	Score	All India rank

9. Research/Industrial/Teaching experience

S.No.	Organisation/ industry served	Date/ Month of entry	Date/ Month of leaving	Months/ Years of experience

10. Journal / Conference Publications(attach a separate page)

11. Any other relevant information

Declaration

I hereby declare that all statements made in this application are true, complete and correct to the best of my knowledge and belief. In the event of the information being found false or incorrect or any ineligibility being detected before or after the selection, my candidature is liable to be cancelled. I confirm, that I am available for ONLINE interview, if necessary.

Date :

Signature of the candidate